

From: [Apon, Daniel C. \(Fed\)](#)
To: [Perlner, Ray A. \(Fed\)](#); [Moody, Dustin \(Fed\)](#)
Subject: Re: new pqc-forum thread -- ok to post? Any edits to the post?
Date: Tuesday, February 19, 2019 4:01:00 PM

I'll send it out.

From: Perlner, Ray (Fed)
Sent: Tuesday, February 19, 2019 3:58:16 PM
To: Apon, Daniel C. (Fed); Moody, Dustin (Fed)
Subject: RE: new pqc-forum thread -- ok to post? Any edits to the post?

Looks fine to me.

From: Apon, Daniel C. (Fed)
Sent: Tuesday, February 19, 2019 3:35 PM
To: Moody, Dustin (Fed) <dustin.moody@nist.gov>; Perlner, Ray (Fed) <ray.perlner@nist.gov>
Subject: Re: new pqc-forum thread -- ok to post? Any edits to the post?

Note: It is certainly ok to post nothing on pqc-forum about this trip (but then again, the group-budget is spending like \$2500-3000 on sending me there, and I want it to be worthwhile to us all..)

From: Apon, Daniel C. (Fed)
Sent: Tuesday, February 19, 2019 3:30:16 PM
To: Moody, Dustin (Fed); Perlner, Ray (Fed)
Subject: new pqc-forum thread -- ok to post? Any edits to the post?

Hi all,

I will be attending the FPGA 2019 conference this upcoming weekend to speak with some hardware teams that may be interested in contributing additional performance measurement data toward NIST's 2nd Round PQC process.

If you are from FPGA 2019 and want to get in touch, or if you are an independent hardware team/company and want to get in touch, or if you are a 2nd Round team and are interested in either (i) having free, additional hardware support for your candidate-algorithm or (ii) (more generally) want to collaborate in our hardware performance analysis process in any way:

Please send an email to pqc-comments@nist.gov (and/or daniel.apon@nist.gov)

Thank you,
--Daniel Apon